

Fig. 2



TITLE: INTEGRATED CIRCUIT PACKAGE ALIGNMENT
FEATURE
Inventor: David J. Corisis et al.
Serial No.: 09/819,874
Docket No.: 3770.3US

1/7

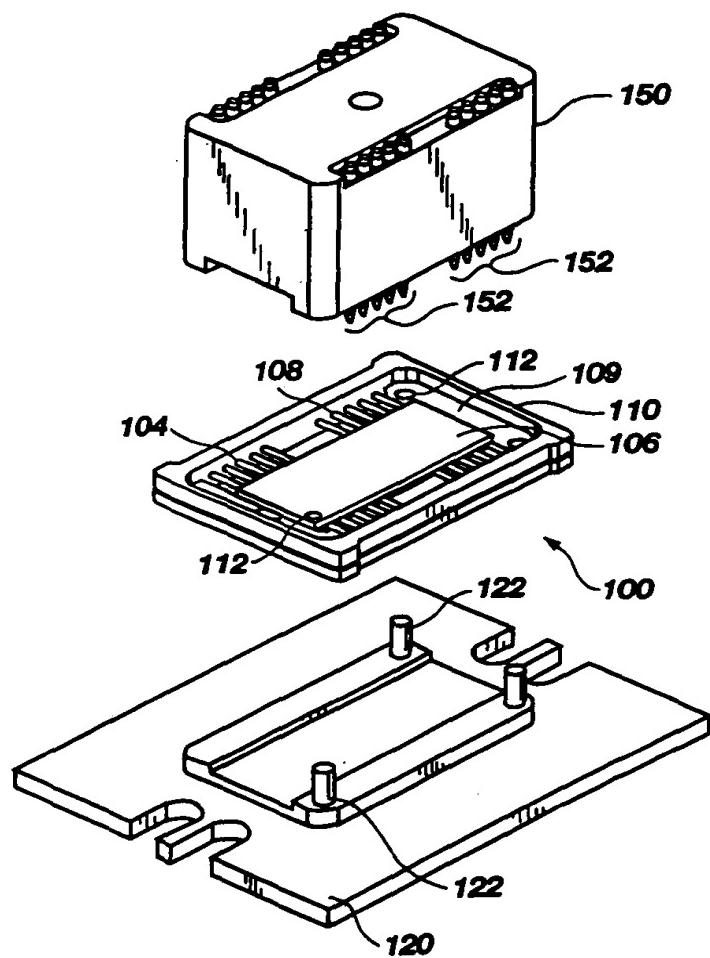


Fig. 1
(PRIOR ART)

2/7

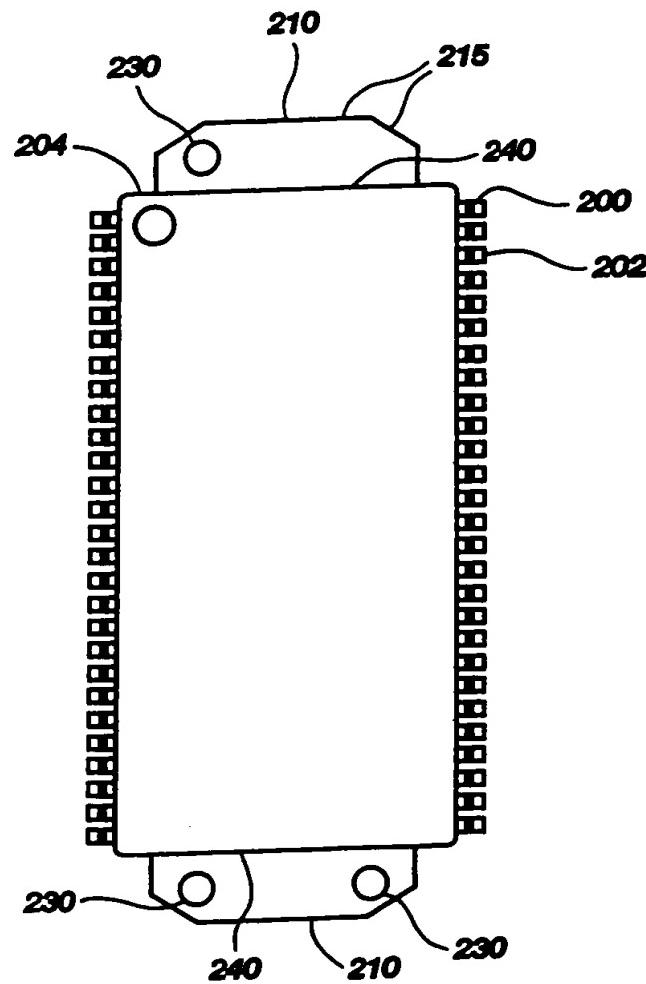


Fig. 2



TITLE: INTEGRATED CIRCUIT PACKAGE FEATURE

Inventor: David J. Corisis et al.

Serial No.: 09/819,874

Docket No.: 3770.3US

3/7

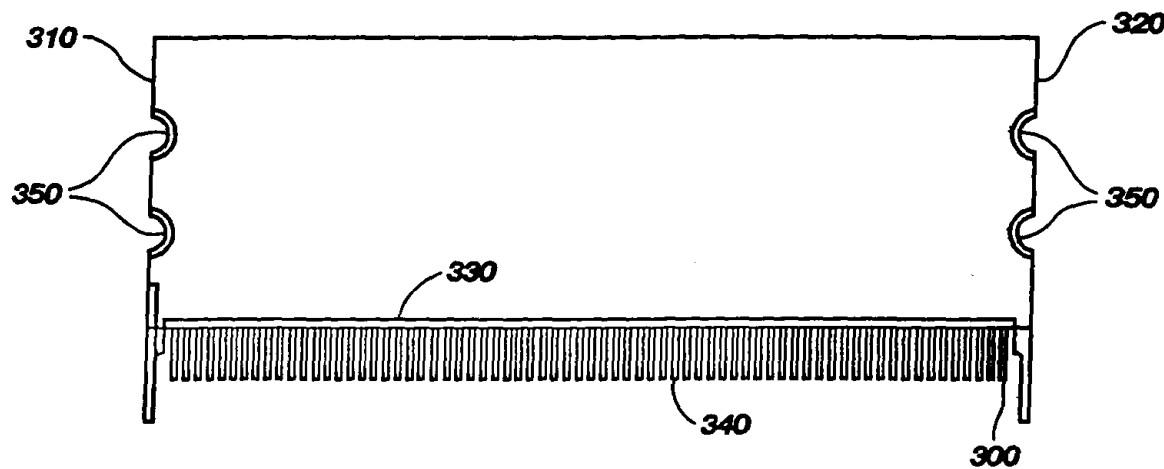


Fig. 3



4/7

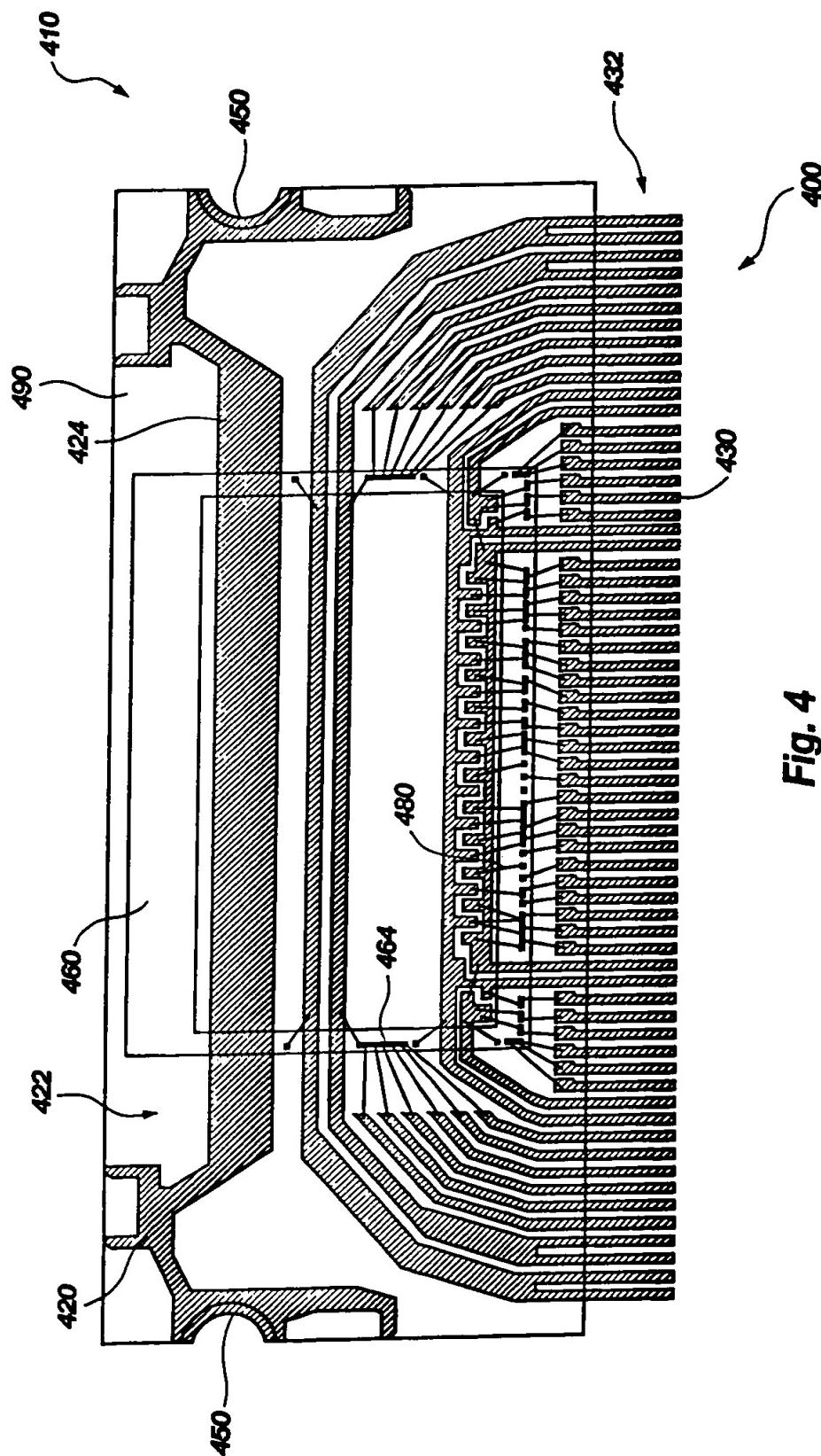
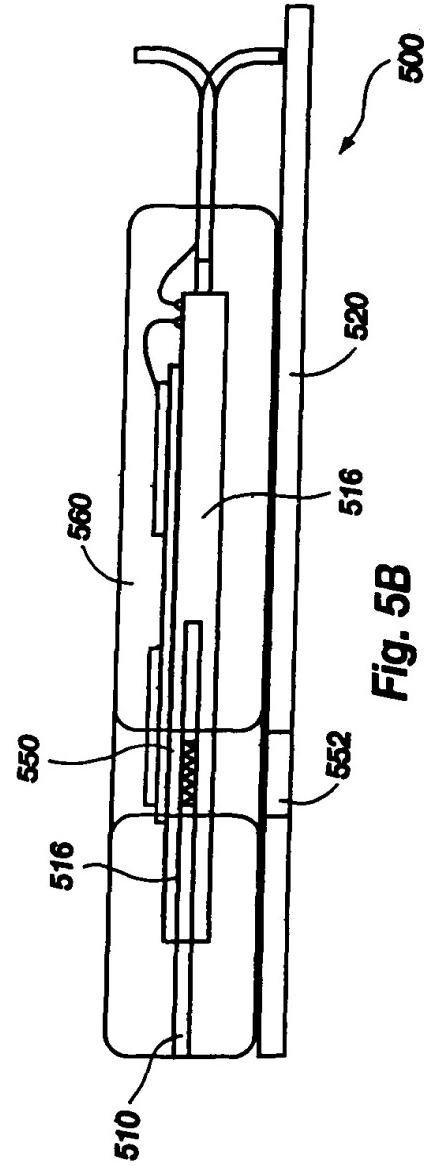
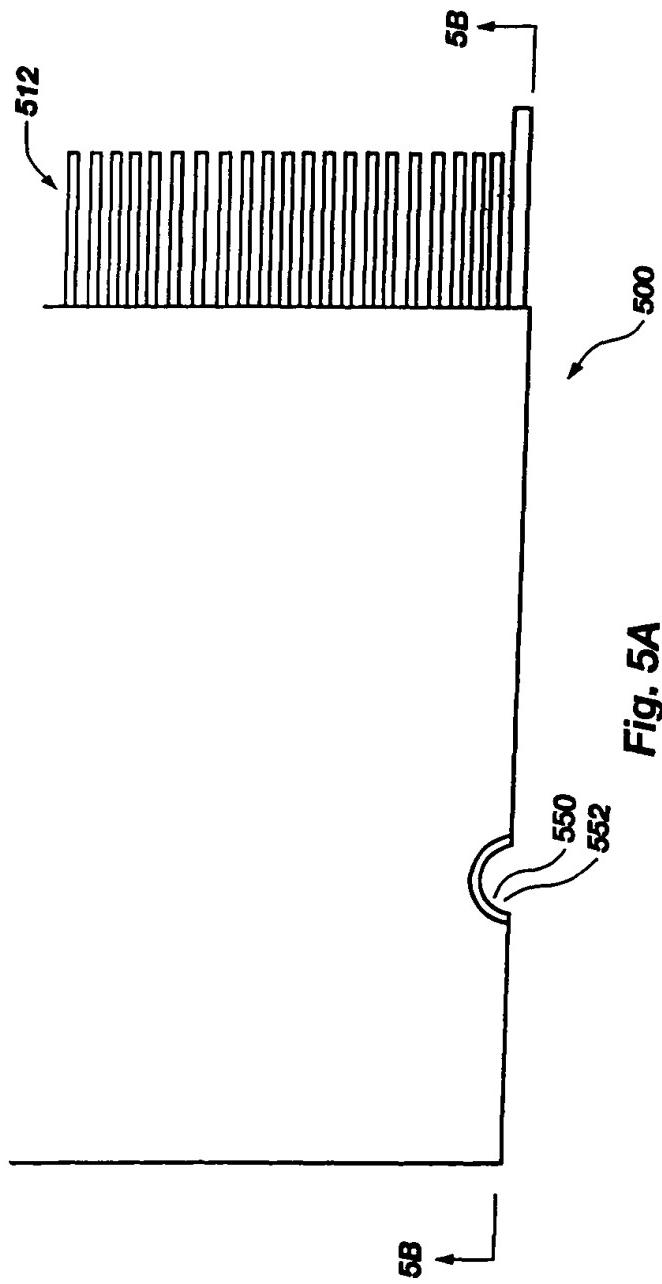
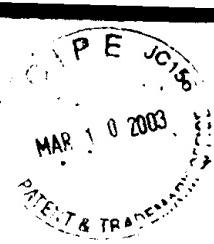


Fig. 4

5/7





TITLE: INTEGRATED CIRCUIT PACKAGE FEATURE
Inventor: David J. Corisis et al.
Serial No.: 09/819,874
Docket No.: 3770.3US

6/7

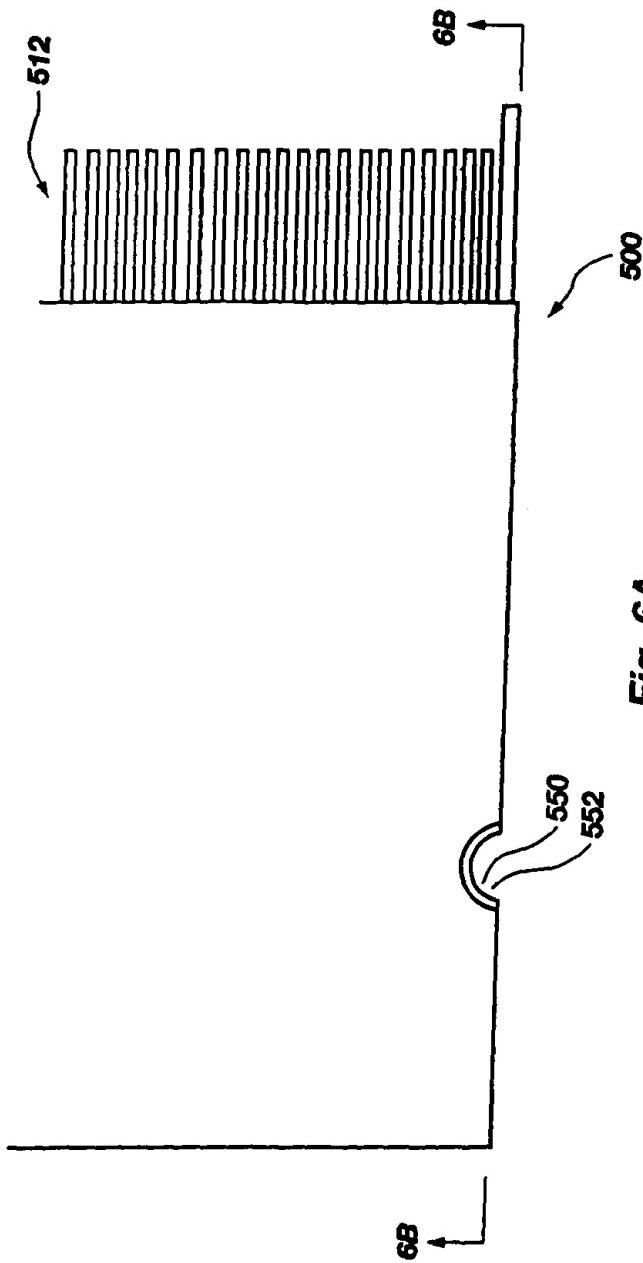


Fig. 6A

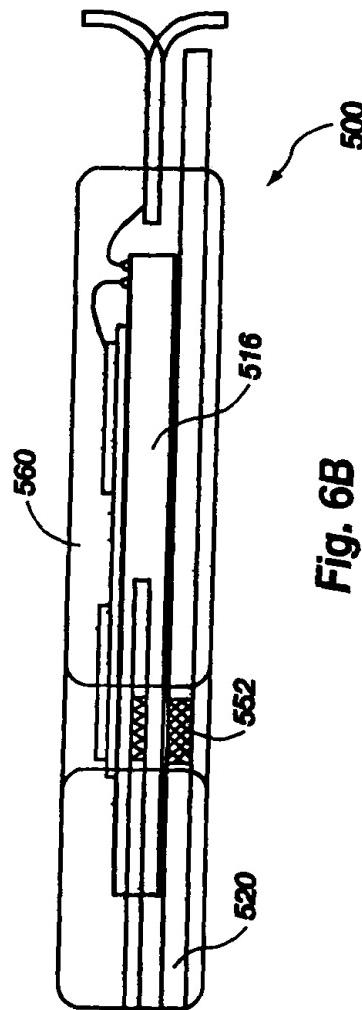


Fig. 6B

P E JC18
MAR 1 0 2003
PATENT & TRADEMARK OFFICE

TITLE: INTEGRATED CIRCUIT PACKAGE FEATURE
Inventor: David J. Corisis et al.
Serial No.: 09/819,874
Docket No.: 3770.3US

7/7

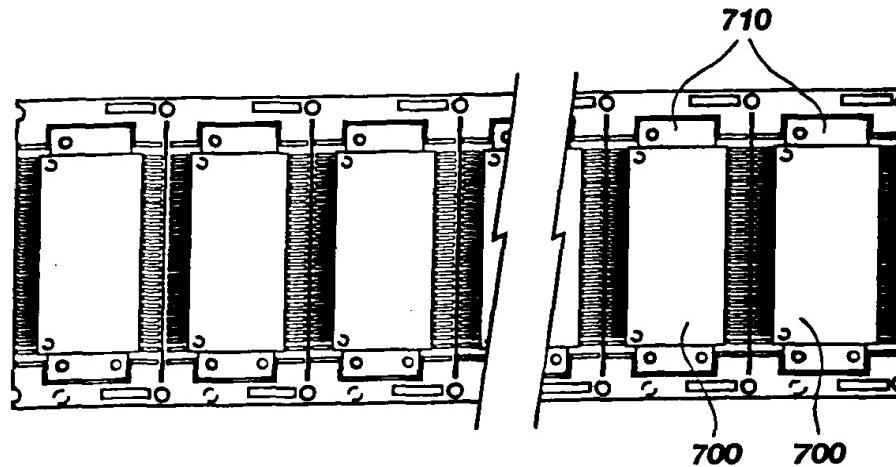


Fig. 7

720

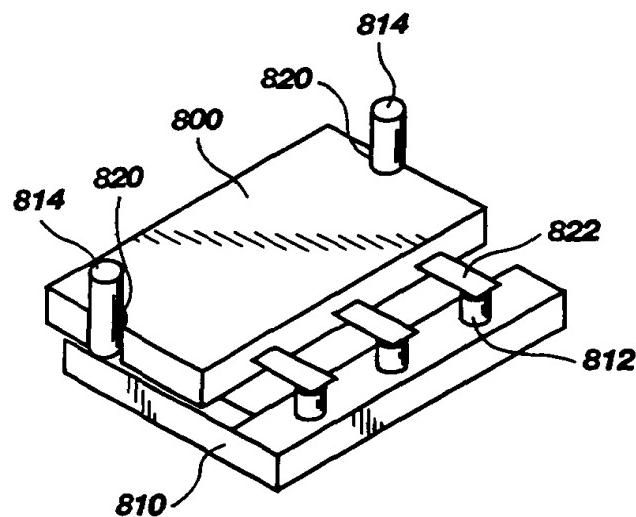


Fig. 8